

PRIOR ART
Fig. 1



MARVELL



MV Interface Description

This interface uses additional pins to provide Tag/mode selection info for future implementations of high latency RDC designs with latency could be more than one sector long

MCMD timing chart for Write/Read operations

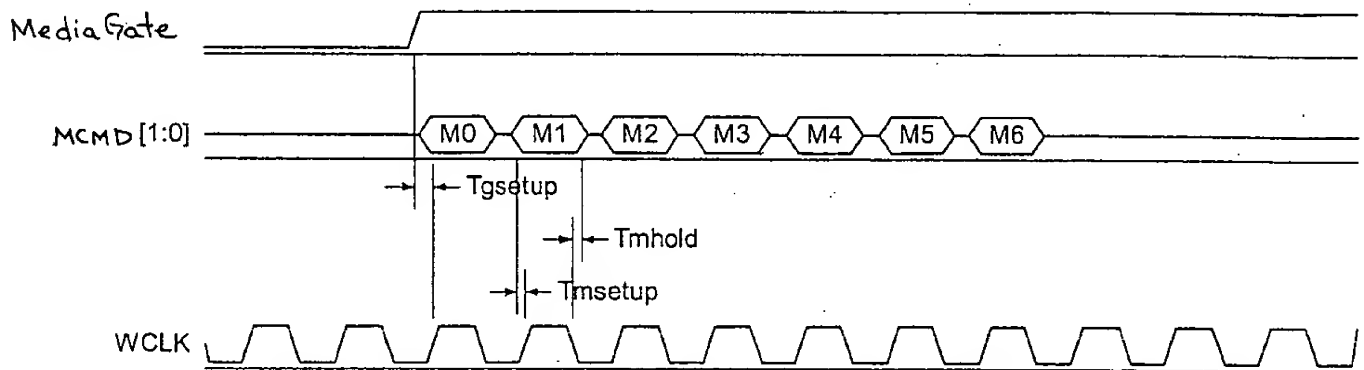


Fig. 3



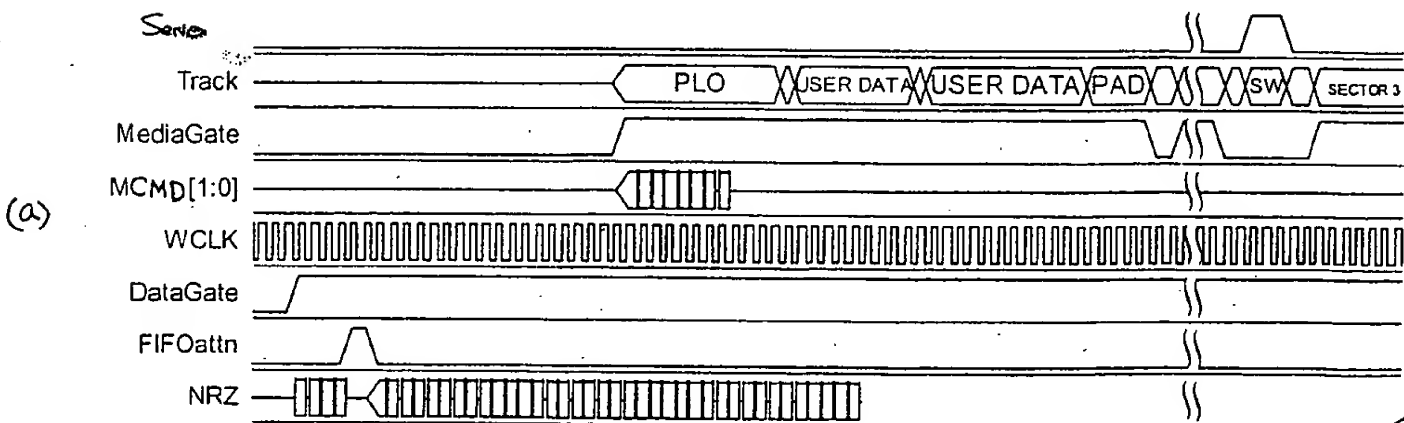
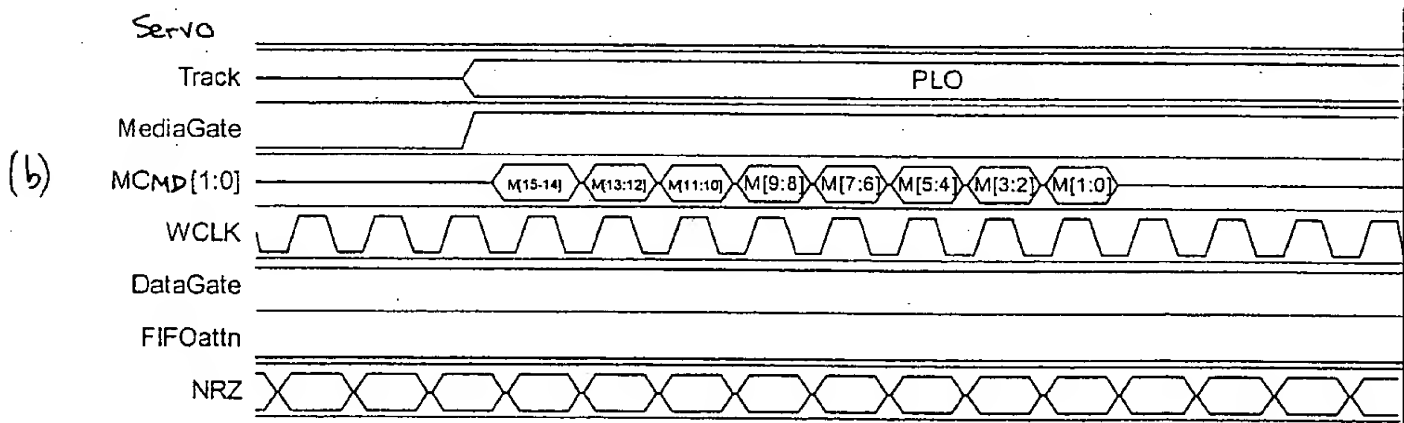
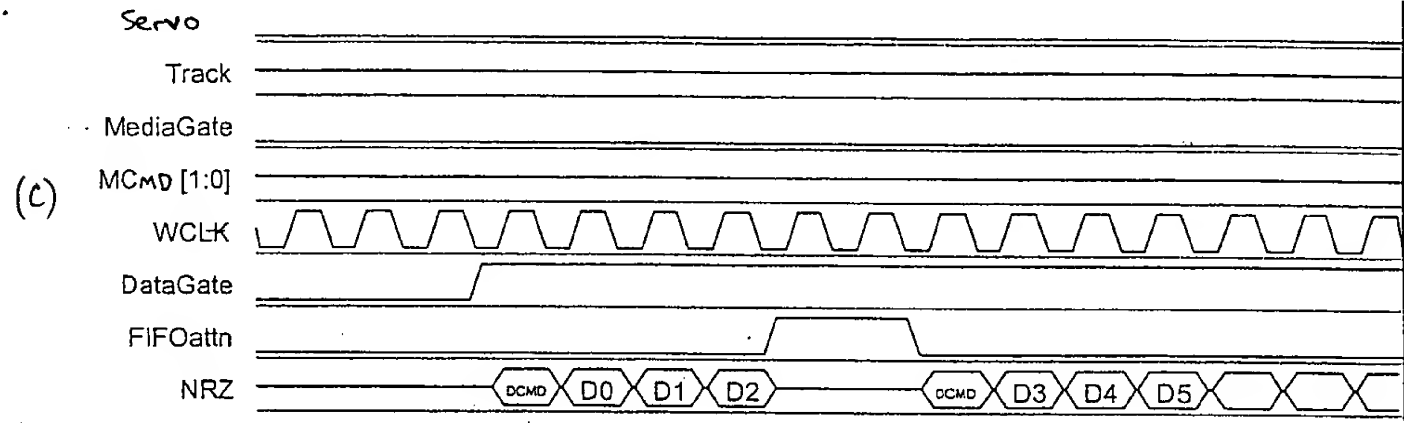
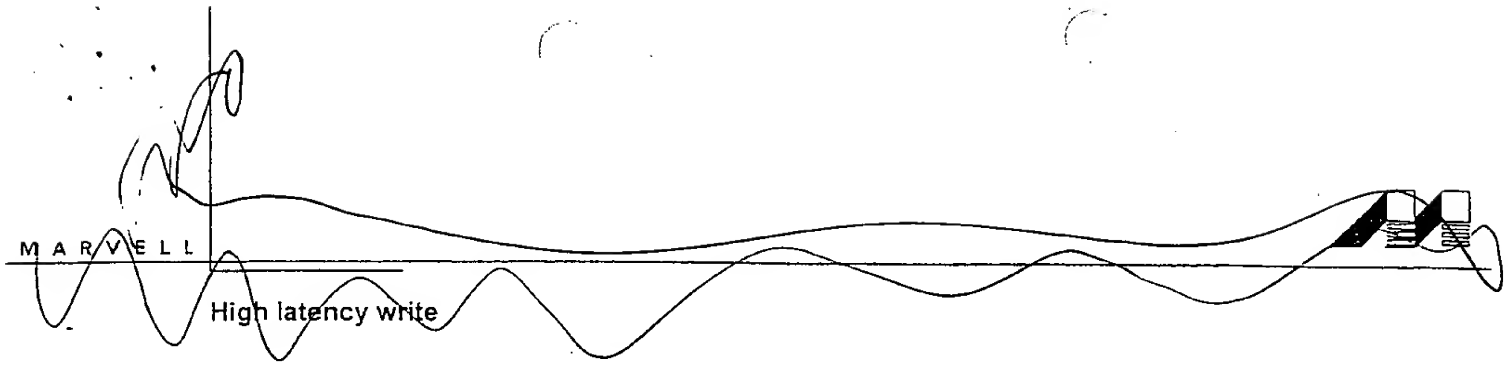


Fig. 4

